EXHIBIT B – Defendants' Terms and Supporting Evidence

No.	Term	Asserted Claims	Defendants' Construction and Supporting Evidence[1]
1	transceiver	Family 1 '686 Patent, Claims 17, 36, 40 Family 2 '881 Patent, Claims 17, 18, 21, 23, 25, 26, 29, 31, 33, and 37 '193 Patent, Claims 1, 9, 10, 11, 12, 13 '601 Patent, Claims 8, 9, 13, 14, 15, 16, 17, 18, 21 '014 Patent, Claims 1, 3 Family 3 '882 Patent, Claims 9, 13'048 Patent, Claims 1, 5 '5473 Patent, Claims 10, 28 '608 Patent, Claims 1, 2, 3, 4 '510 Patent, Claims 21, 22	Proposed Construction Plain and ordinary meaning, which is "communications device capable of transmitting and receiving data" Extrinsic Evidence[2] • 1998 & 2004 Merriam-Webster Dictionary definition of "transceiver": a radio transmitter-receiver that uses many of the same components for both transmission and reception • 2002 McGraw-Hill Dictionary of Scientific and Technical Terms definition of "transceiver": A radio transmitter and receiver combined in one unit and having switching arrangements such as to permit both transmitting and receiving • U.S. Patent No. 7,844,882 at 5:18-57 • ITU-T G.993.2 VDSL2 Standard • ITU-T G.992.1 ADSL Standard Family 1 Intrinsic Evidence • '686 Patent at 1:24-2:67.

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[1] Where patents in a patent family share specifications, an identification of intrinsic evidence as to one patent in the family equally identifies the equivalent passage for the remaining patents in the family.

[2] Defendants reserve the right to rely upon any claim construction briefs, declarations, transcripts, orders, and memorandum opinions, including all intrinsic and extrinsic evidence cited therein, in any other case in which TQ Delta alleged infringement of one or more of the Asserted Patents and/or related patents, including the consolidated cases *TQ Delta LLC v. 2Wire, Inc.*, No. 1:13-cv-01835-RGA (D. Del.) and *TQ Delta LLC v. ADTRAN Inc.*, No. 1:14-cv-00954-RGA (D. Del.).

EXHIBIT B – Defendants' Terms and Supporting Evidence

No.	Term	Asserted Claims	Defendants' Construction and Supporting Evidence[1]
		Family 4 '008 Patent, Claim 14 Family 6 '835 Patent, Claims 8, 24 '112 Patent, Claims 8, 10, 11, 12, 14 Family 9 '411 Patent, Claims 10, 11, 17, 18, 19, 25 '577 Patent, Claims 16, 30, 37, 38, 53, 54 '348 Patent, Claims 1, 3, 9, 11 '055 Patent, Claims 11, 17, 19 '4473 Patent, Claims 1, 3 '809 Patent, Claims 1, 3, 4, 6, 8, 10, 11, 13, 15, 17, 18, 20, 22, 25, 27 Family 10 '354 Patent, Claims 10–12 '988 Patent, Claim 16	Family 2 Intrinsic Evidence • '193, '601, '014 Patents at Abstract, Figure 1, & Figure 2 Family 3 Intrinsic Evidence • '882 Patent at 5:18–57 • '5473 Patent at 5:23–62 • '608 Patent at 5:35–6:7 • '510 Patent at 5:41–6:14 • '048 Patent at 5:23–62 Family 4 Intrinsic Evidence • '008 patent at Figure 1, 1:13-18, 1:25-60:2, 3:24-51 Family 6 Intrinsic Evidence • '835 patent at Abstract, 2:34-51, 3:28-31, 4:32-36, 5:7-10, 7:67-8:8 Family 9 Intrinsic Evidence • '411, '577, '348, '055, '4473, '809 patents at Abstract • '411, '577, '348, '055, '4473, '809 patents at Fig. 1 • '411 Patent at 1:33–2:22, 2:51–8:34, 9:26–10:5, 13:6–16, 20:38–43 • '577 Patent at 1:30–2:18, 2:47–8:31, 9:23–10:2, 13:4–14, 20:38–43

EXHIBIT B – Defendants' Terms and Supporting Evidence

No.	Term	Asserted Claims	Defendants' Construction and Supporting Evidence[1]
			• '348 Patent at 1:33–2:22, 2:51-8:34, 9:26-10:5, 13:6-16, 20:38-43 • '055 Patent at 1:37–2:27, 1:56–8:46, 9:39–10:21, 13:30–41, 21:1-6 • '4473 Patent at 1:39–2:29, 2:58–8:46, 9:39–10:21, 13:30–41, 21:4–9 • '809 Patent at 1:34–2:36, 3:1–8:57, 9:50–10:31, 13:40–51, 21:11–17 Family 10 Intrinsic Evidence • '354 Patent at Abstract, 2:17–32, 2:49–3:2, 4:8–9, 6:18–63, 7:1–3 • '988 Patent at Abstract, 2:14–29, 245–65, 4:5–6, 6:13–57, 6:62–64
2	each bit in the diagnostic message is mapped to [at least one / one] DMT symbol	Family 1 '686 Patent, Claims 17, 36, 40	Proposed Construction Indefinite Extrinsic Evidence • 2002 McGraw-Hill Dictionary of Scientific and Technical Terms definition of "map": An output produced by an assembler, compiler, linkage editor, or relocatable loader which indicates the (absolute or relocatable) locations of such elements as programs, subroutines, variables, or arrays. • 2002 McGraw-Hill Dictionary of Scientific and Technical Terms definition of "mapping": Preparation of a map or engaging in a mapping operation. 1. Any function or multiple-valued relation.

EXHIBIT B – Defendants' Terms and Supporting Evidence

No.	Term	Asserted Claims	Defendants' Construction and Supporting Evidence[1]
			• Wikipedia.com definition of "map": This terminology is not completely fixed, as these terms are generally not formally defined, and can be considered to be jargon. • Google definition of "mapped": "associate (a group of elements or qualities) with an equivalent group, according to a particular formula or model" • ITU-T G.993.2 VDSL2 Standard • ITU-T G.992.1 ADSL Standard • Expert Declaration of Bruce McNair Intrinsic Evidence • '686 patent at 3:54-67. • '686 File History, October 31, 2006 Non-Final Rejection; January 12, 2007 Amendment and Remarks • '686 File History, March 30, 2007 Final Rejection; April 29, 2008 Amendment, RCE and Arguments
3	array representing frequency domain received idle channel noise information	Family 1 '686 Patent, Claims 17, 36, 40	Proposed Construction "array of values representative of noise in the frequency domain that was received by a transceiver on respective subchannels in the absence of a transmission signal" Extrinsic Evidence • Expert Declaration of Bruce McNair Intrinsic Evidence • '686 Patent at 4:25-54.

EXHIBIT B – Defendants' Terms and Supporting Evidence

No.	Term	Asserted Claims	Defendants' Construction and Supporting Evidence[1]
			• '686 File History, April 29, 2008 RCE and Amendment and Remarks • '686 File History, October 20, 2008 Non-Final Rejection • '686 File History, February 26, 2009 Amendments and Remarks
4	plurality of bonded transceivers	Family 2 '881 Patent, Claims 17, 18, 21, 23, 25, 26, 29, 31, 33, 37	Proposed Construction two or more transceivers, located on the same side of two or more physical links and each corresponding to one of the physical links, coordinated to transmit or receive a different portion of the same bit stream via a different one of the physical links Intrinsic Evidence • '881 patent, Fig. 2; 1:42-56; 1:60-2:5; 2:10-19; 3:58-4:38; 4:57-5:6; 5:44-52; 10:54-58; 11:31-34
5	operable to	Family 2 '193 Patent, Claim 13 '601 Patent, Claims 8, 14, 21 '014 Patent, Claim 1 Family 3 '608 Patent, Claims 1, 4 '510 Patent, Claim 21, 22	Proposed Construction Plain and ordinary meaning, not mere capability Intrinsic Evidence Family 6: • '112 Patent at Abstract Family 9: • '577 Patent at 23:31–26:54 • '348 Patent at 23:30–25:15

EXHIBIT B – Defendants' Terms and Supporting Evidence

No.	Term	Asserted Claims	Defendants' Construction and Supporting Evidence[1]
		Family 6 '112 Patent, Claim 8 Family 9 '577 Patent, Claims 16, 30, 38, 53, 54 '348 Patent, Claims 1, 3, 9, 11 '055 Patent, Claim 11 '4473 Patent, Claims 1, 3 Family 10 '354 Patent, Claims 10 '988 Patent, Claim 16	• '055 Patent at 23:65–25:11 • '4473 Patent at 24:2–26:45 Extrinsic Evidence • '577 Patent at 23:31–26:54 • '348 Patent at 23:30–25:15 • '055 Patent at 23:65–25:11 • '4473 Patent at 24:2–26:45
6	reduce a difference in latency between the bonded transceivers	Family 2 '881 Patent, Claims 17, 25, 26, 29, 31, 33, 37	Proposed Construction Indefinite, or, if not indefinite, "minimize the difference in the configuration latencies between the bonded transceivers" Extrinsic Evidence • '706 Patent File History – Office Action dated 1/10/2011; Amdmt and Response Dated 2/11/2011. • '511 Patent File History – Office Action dated 2/15/2011; Amdmt and Resp. dated 8/11/2011; Office Action dated Nov. 18, 2011; Amdmt dated May 14, 2012 • Expert Declaration of George Zimmerman

EXHIBIT B – Defendants' Terms and Supporting Evidence

No.	Term	Asserted Claims	Defendants' Construction and Supporting Evidence[1]
			Intrinsic Evidence • '881 patent, Fig. 15; 5:23-42; 6:1-35; 6:56-7:36; 10:40-47 • '881 Patent File History – Office Action dated 10/18/2006; Amendment dated 2/28/2007
7	each bonded transceiver utilizing at least one transmission parameter value to reduce a difference in latency between the bonded transceivers	Family 2 '881 Patent, Claims 17, 25, 26, 29, 31, 33, 37	Proposed Construction Indefinite, or, if not indefinite, "each bonded transceiver configured with at least one transmission parameter value to minimize the difference in the configuration latencies between the bonded transceivers" Extrinsic Evidence • '706 Patent File History – Office Action dated 1/10/2011; Amdmt and Response Dated 2/11/2011. • '511 Patent File History – Office Action dated 2/15/2011; Amdmt and Resp. dated 8/11/2011; Office Action dated Nov. 18, 2011; Amdmt dated May 14, 2012 • Expert Declaration of George Zimmerman Intrinsic Evidence • '881 patent, Fig. 15; 5:23-42; 6:1-35; 6:56-7:36; 10:40-47 • '881 Patent File History – Office Action dated 10/18/2006; Amendment dated 2/28/2007

EXHIBIT B – Defendants' Terms and Supporting Evidence

No.	Term	Asserted Claims	Defendants' Construction and Supporting Evidence[1]
8	utilize at least one transmission parameter value, for each transceiver in a plurality of bonded transceivers, to reduce a difference in latency between the bonded transceivers	Family 2 '881 Patent, Claims 33, 37	Proposed Construction Indefinite, or, if not indefinite, "configure at least one transmission parameter value of each bonded transceiver to minimize the difference in the configuration latencies between the bonded transceivers" Extrinsic Evidence • '706 Patent File History – Office Action dated 1/10/2011; Amdmt and Response Dated 2/11/2011. • '511 Patent File History – Office Action dated 2/15/2011; Amdmt and Resp. dated 8/11/2011; Office Action dated Nov. 18, 2011; Amdmt dated May 14, 2012 • Expert Declaration of George Zimmerman Intrinsic Evidence • '881 patent, Fig. 15; 5:23-42; 6:1-35; 6:56-7:36; 10:40-47 • '881 Patent File History – Office Action dated 10/18/2006; Amendment dated 2/28/2007
9	utilize at least one parameter associated with operation of at least one of the first and second transceivers to reduce a difference in latency between the first and second transceivers	Family 2 '193 Patent, Claim 13 '601 Patent, Claims 14, 21	Proposed Construction Indefinite Extrinsic Evidence • '706 Patent File History – Office Action dated 1/10/2011; Amdmt and Response Dated 2/11/2011.

EXHIBIT B – Defendants' Terms and Supporting Evidence

No.	Term	Asserted Claims	Defendants' Construction and Supporting Evidence[1]
			• '511 Patent File History – Office Action dated 2/15/2011; Amdmt and Resp. dated 8/11/2011; Office Action dated Nov. 18, 2011; Amdmt dated May 14, 2012 • Expert Declaration of George Zimmerman Intrinsic Evidence • '193 patent at Figure 15, Figure 16, 1:30-36, 6:7-7:40, 10:54-11:13. • '601 Patent at Figure 15, Figure 16, 1:32-39, 6:8-7:44, 10:54-11:13. • '193 File History: September 26, 2014 Preliminary Amendment and Remarks • '601 File History: February 28, 2007 Amendment
10	"shared memory" / "sharing the memory" / "operable to be shared" / "sharing"	Family 3 '882 Patent, Claims 9, 13 '048 Patent, Claims 1, 5 '5473 Patent, Claims 10 '510 Patent, Claims 21, 22 '608 Patent, Claim 2	Proposed construction Plain and ordinary meaning Extrinsic Evidence • Expert Declaration of Rick Wesel Intrinsic Evidence • '882 File History: Notice of Allowance, October 6, 2010 • '048 File History: Notice of Allowance, July 27, 2012 • '5473 File History: Notice of Allowance, May 21, 2013 • '608 File History: Notice of Allowance, September 23, 2016

EXHIBIT B – Defendants' Terms and Supporting Evidence

No.	Term	Asserted Claims	Defendants' Construction and Supporting Evidence[1]
			• '510 File History: Notice of Allowance, June 13, 2019
11	"wherein the generated message indicates how the memory has been allocated between the [first deinterleaving / interleaving] function and the [second] deinterleaving function" / "a message indicating how the shared memory is to be used by the interleaver or the deinterleaver"	Family 3 '5473 Patent, Claims 10, 28	Proposed Construction Plain and ordinary meaning, i.e., "the message indicates the amount of memory [that has been allocated to / is to be used by] the [first deinterleaving / interleaving] function and the amount of memory [that has been allocated to / is to be used by] the [second] deinterleaving function" Extrinsic Evidence • Expert Declaration of Rick Wesel Intrinsic Evidence • '5473 Patent at 6:16–7:54; 11:8–15; 12:10–23
12	specifying a maximum number of bytes of memory that are available to be allocated to [a/an interleaver/deinterleaver]	Family 3 '882 Patent, Claims 9, 13 '048 Patent, Claims 1, 5	Proposed Construction Plain and ordinary meaning Intrinsic Evidence: • '882 Patent at Abstract; 1:66–2:4; 2:65–67; 6:4–11; 7:26–9:4; 10:40–65; 11:5–32; 11:39–12:12; 12:19–43; Figure 3 • '048 Patent at Abstract; 2:3–9; 3:1–3; 6:9–15; 7:30–9:6; 10:41–65; 11:5–11

EXHIBIT B – Defendants' Terms and Supporting Evidence

No.	Term	Asserted Claims	Defendants' Construction and Supporting Evidence[1]
13	substantially scramble the phase characteristics of the plurality of carrier signals	Family 4 '008 Patent, Claim 14	Proposed Construction Plain and ordinary meaning Intrinsic Evidence: • '008 Patent at Abstract, Figure 1, Figure 2, 1:48-59, 2:15-3:3, 3:13-21, 4:29-5:31, 6:29-8:22, 9:4-28, 9:53-10:4
14	multiple carrier signals corresponding to the scrambled carrier signals are used by the first multicarrier transceiver to modulate the same bit value (proposed by Defendants) / same bit value (proposed by Plaintiff)	Family 4 '008 Patent, Claim 14	Proposed Construction Indefinite Extrinsic Evidence • Expert Declaration of George Zimmerman Intrinsic Evidence • '134 provisional at 1-2 • '008 patent at Abstract, 2:15-3:3, 3:63-4:28, 4:48-5:4, 9:16-28 • Applicant Arguments/Remarks Made in an Amendment dated Aug. 11, 2011 • Applicant Arguments/Remarks Made in an Amendment dated Oct. 3, 2011 • Notice of Allowance, Nov. 17, 2011
15	computing a phase shift for each carrier signal	Family 4 '008 Patent, Claim 14	Proposed Construction

EXHIBIT B – Defendants' Terms and Supporting Evidence

No.	Term	Asserted Claims	Defendants' Construction and Supporting Evidence[1]
			computing the amount by which a phase is adjusted for each carrier signal Intrinsic Evidence • '008 patent at Abstract, Figure 1, Figure 2, 1:48-59, 2:15-3:3, 4:29-5:31, 6:29-8:22, 9:4-28, 9:53-10:4
16	combining the phase shift computed for each respective carrier signal with the phase charac-teristic of that carrier signal	Family 4 '008 Patent, Claim 14	Proposed Construction adjusting the phase of each carrier signal by an amount computed for that carrier signal Intrinsic Evidence • '008 patent at Abstract, Figure 1, Figure 2, 1:48-59, 2:15-30, 3:3, 3:57-62, 4:29-5:31, 6:29-8:22, 9:4-28, 9:53-10:4
17	phase characteristic[3]	Family 4 '008 Patent, Claim 14	Proposed Construction plain and ordinary meaning Intrinsic Evidence • '008 patent at Abstract, Figure 2, 1:33-47, 2:15-3:3, 3:13-21, 3:63-4:47, 5:14-31, 6:41-8:14, 9:4-28, 9:47-10:4

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[3] Plaintiff did not identify this term in its P.R. 4-1 disclosures, and Defendants accordingly object to Plaintiff's attempt to belatedly propose this term for construction.

EXHIBIT B – Defendants' Terms and Supporting Evidence

No.	Term	Asserted Claims	Defendants' Construction and Supporting Evidence[1]
18	FIP setting	Family 6 '835 Patent, Claims 8, 10, 24, 26 '112 Patent, Claim 8	Proposed Construction forward error correction and interleaver parameters characterized by the set of parameters for codeword size in bytes, number of information bytes in a codeword, number of parity or redundancy bytes in a codeword, and interleaver depth in number of codewords Extrinsic Evidence • Expert declaration of Bruce McNair • ITU-T G992.3 Standard Intrinsic Evidence • '835 patent at Abstract, Figure 3, Figure 4, Figure 5, Figure 6, 1:26-44, 1:44-2:33, 3:27-4:42, 5:52-6:15, 8:4-9:39, 12:8-24, 12:39-13:53, 19:15-30
19	FIP value	Family 6 '835 Patent, Claims 8, 24	Proposed Construction numerical value of codeword size in bytes, number of information bytes in a codeword, number of parity or redundancy bytes in a codeword, or interleaver depth in number of codewords Extrinsic Evidence • Expert declaration of Bruce McNair • ITU-T G992.3 Standard Intrinsic Evidence

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EXHIBIT B – Defendants' Terms and Supporting Evidence

No.	Term	Asserted Claims	Defendants' Construction and Supporting Evidence[1]
			'835 patent at Abstract, Figure 1, Figure 3, Figure 4, Figure 5, Figure 6, 1:26-44, 1:44-2:33, 3:27-4:42, 5:52-6:15, 8:4-9:39, 12:8-24, 12:39-13:53, 19:15-30
20	flag signal	Family 6 '835 Patent, Claims 8, 24 '162 Patent, Claims 8, 9	Proposed Construction signal used to indicate when updated FIP settings / interleaver parameter values are to be used. Intrinsic Evidence • '835 patent at Abstract, Figure 6, 8:50-65, 11:4-12:37, 18:47-19:14, 19:15-30
21	interleaver parameter value	Family 6 '835 Patent, Claims 10, 26 '162 Patent, Claim 8	Proposed Construction the numerical value of the interleaver depth in number of codewords Extrinsic Evidence • Expert declaration of Bruce McNair Intrinsic Evidence • '835 patent at Abstract, Figure 1, Figure 3, Figure 4, Figure 5, Figure 6, 1:26-44, 1:44-2:33, 3:27-4:42, 5:52-6:15, 8:4-9:39, 12:8-24, 12:39-13:53, 19:15-30
22	steady-state communication	Family 6 '112 Patent, Claim 8 '835 Patent, Claim 8 and 24	Proposed Construction

EXHIBIT B – Defendants' Terms and Supporting Evidence

No.	Term	Asserted Claims	Defendants' Construction and Supporting Evidence[1]
			the state of the transceiver reached after all initialization and training is completed in which user data is transmitted or received Intrinsic Evidence • '835 patent at Figure 2, Figure 3, Figure 4, Figure 5, Figure 6, 1:26-44, 2:34-3:16, 3:27-5:20, 5:52-65, 6:25-55, 6:63-67, 8:4-9:49, 11:10-29, 13:54-14:45, 15:11-48, 19:15-30
23	receive at least one message without using interleaving	Family 9 '577 Patent, Claims 37, 53	Proposed Construction Indefinite Extrinsic Evidence • Expert Declaration of Rick Wesel • ITU-T G.993.1 VDSL1 Standard • ITU-T G.993.2 VDSL2 Standard Intrinsic Evidence • '577 Patent at 1:21–26, 9:56–10:2, 9:38–46, 23:32–26:54
24	Memory has been allocated	Family 9 '411 Patent, Claim 10	Proposed Construction Plain and Ordinary meaning Intrinsic Evidence • '411 Patent at 12:4–8, 21:4–11

EXHIBIT B – Defendants' Terms and Supporting Evidence

No.	Term	Asserted Claims	Defendants' Construction and Supporting Evidence[1]
25	[transmit / transmitting / retransmit / retransmitting / receive / receiving] [by the transceiver] [at least one packet / a packet / the packet / a retransmitted packet / a message / a plurality of messages / at least one message] using [interleaving / deinterleaving / (a/the) forward error correction encoder / (a/the) forward correction decoder / forward correction encoding / forward correction decoding] [and (an/the) interleaver / and (a/the) deinterleaver / and interleaving / and deinterleaving]	Family 9 '577 Patent, Claims 16, 30, 38, 54 '348 Patent, Claims 1, 3, 9, 11 '4473 Patent, Claims 1, 3 '809 Patent, Claims 1, 3, 8, 10, 15, 17, 22	Proposed Construction Indefinite Extrinsic Evidence • Expert declaration of Rick Wesel • ITU-T G.993.1 VDSL1 Standard • ITU-T G.993.2 VDSL2 Standard Intrinsic Evidence • '577 Patent at 1:21–26, 9:56–10:2, 9:38–46, 23:32–26:54 • '348 Patent at 1:24–29, 9:41–49, 9:59–10:5, 23:30–25:15 • '4473 Patent at 1:30–35, 9:55–9:63, 10:7–10:21, 24:2–26:46 • '809 Patent at 1:34–40, 9:66–10:7, 10:18–31, 24:10–26:35
26	higher immunity to noise	Family 9 '348 Patent, Claims 2 and 9 '809 Patent, Claims 1, 9, 16, 23	Proposed Construction Plain and Ordinary meaning Extrinsic Evidence • Expert Declaration of Rick Wesel Intrinsic Evidence

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EXHIBIT B – Defendants' Terms and Supporting Evidence

No.	Term	Asserted Claims	Defendants' Construction and Supporting Evidence[1]
			• '809 Patent at 14:60–16:67 • '348 Patent at 14:24–16:29
27	A multicarrier communications transceiver operable to receive a multicarrier symbol comprising a first plurality of carriers	Family 10 '354 Patent, Claim 10	Proposed Construction Indefinite Extrinsic Evidence • Expert declaration of George Zimmerman Intrinsic Evidence • '354 Patent at Abstract, Fig. 2, 1:30–2:45, 4:14–16
28	receive a first plurality of bits on the first plurality of carriers using a first SNR margin; receive a second plurality of bits on the second plurality of carriers using a second SNR margin	Family 10 '354 Patent, Claim 10	Proposed Construction Indefinite Extrinsic Evidence • Expert declaration of George Zimmerman Intrinsic Evidence • '354 Patent at 2:14–19, 3:27–33, 4:10–11; 5:3–6, 5:20–24, 7:29–36, 9:22–24
29	wherein the first SNR margin provides more robust reception than the second SNR margin	Family 10 '354 Patent, Claim 10	Proposed Construction Indefinite Extrinsic Evidence • Expert declaration of George Zimmerman

EXHIBIT B – Defendants' Terms and Supporting Evidence

No.	Term	Asserted Claims	Defendants' Construction and Supporting Evidence[1]
			Intrinsic Evidence • '354 Patent at 1:45-49, 2:17–40, 3:34–52, 4:65–5:14, 6:4–17
30	Signal to Noise Ratio (SNR) margin	Family 10 '354 Patent, Claims 10–12 '988 Patent, Claim 16	Proposed Construction a parameter used in determining the number of bits allocated to each of a plurality of carriers, where the value of the parameter specifies an extra SNR requirement assigned per carrier in addition to the SNR required to maintain a specified bit error rate (BER) for the communication link at a specified bit allocation Intrinsic Evidence • '354 Patent at Abstract, 2:4–16, 3:31–33, 3:62–67, 4:10–11, 4:14–6:17, 7:57–8:24, 9:22–31 • '988 Patent at Abstract, 2:1–13, 3:27–29, 3:59–64, 4:7–8, 4:11–6:12, 7:49–8:16, 9:12–21